

Fig. 7

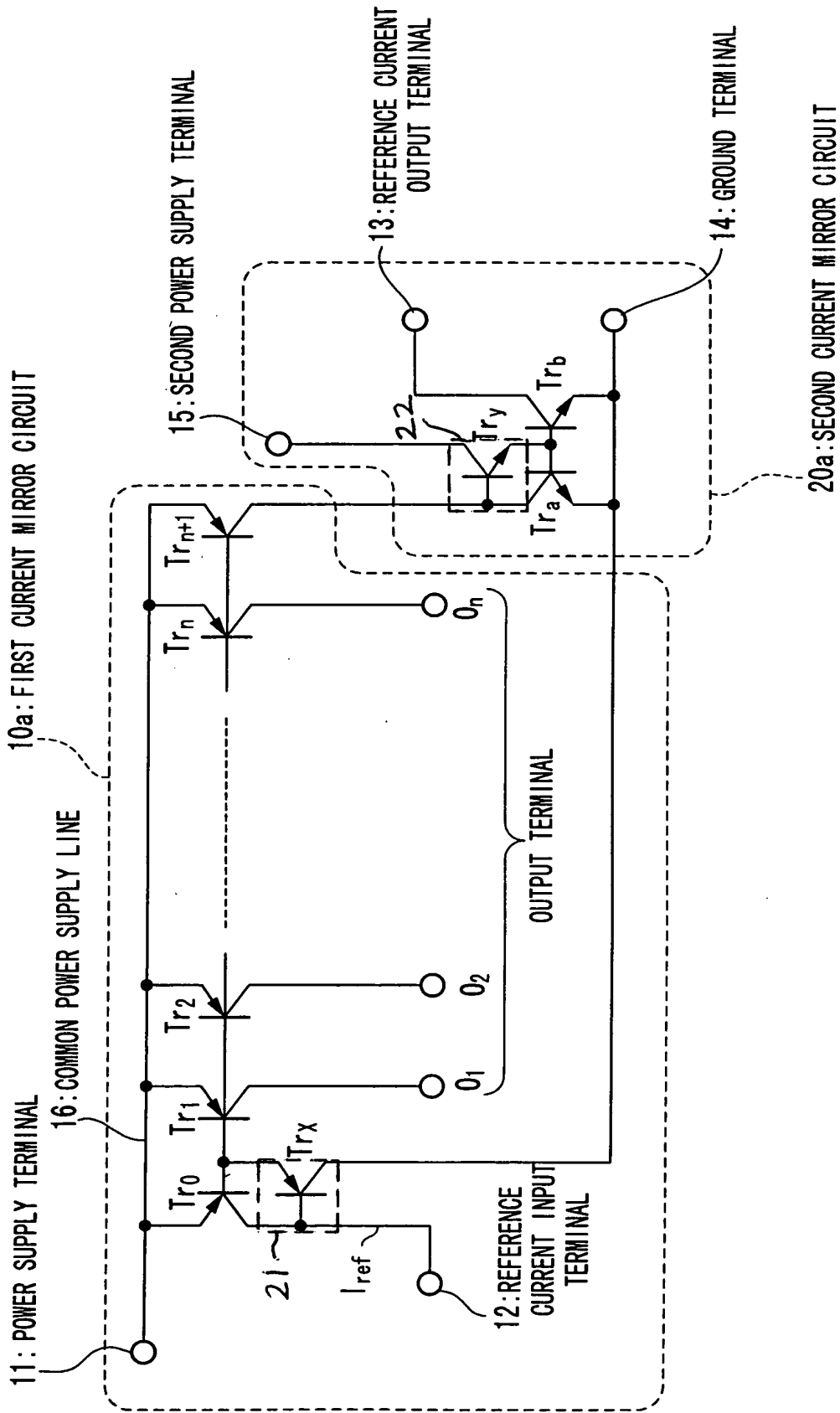
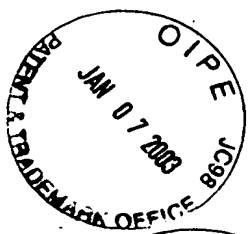
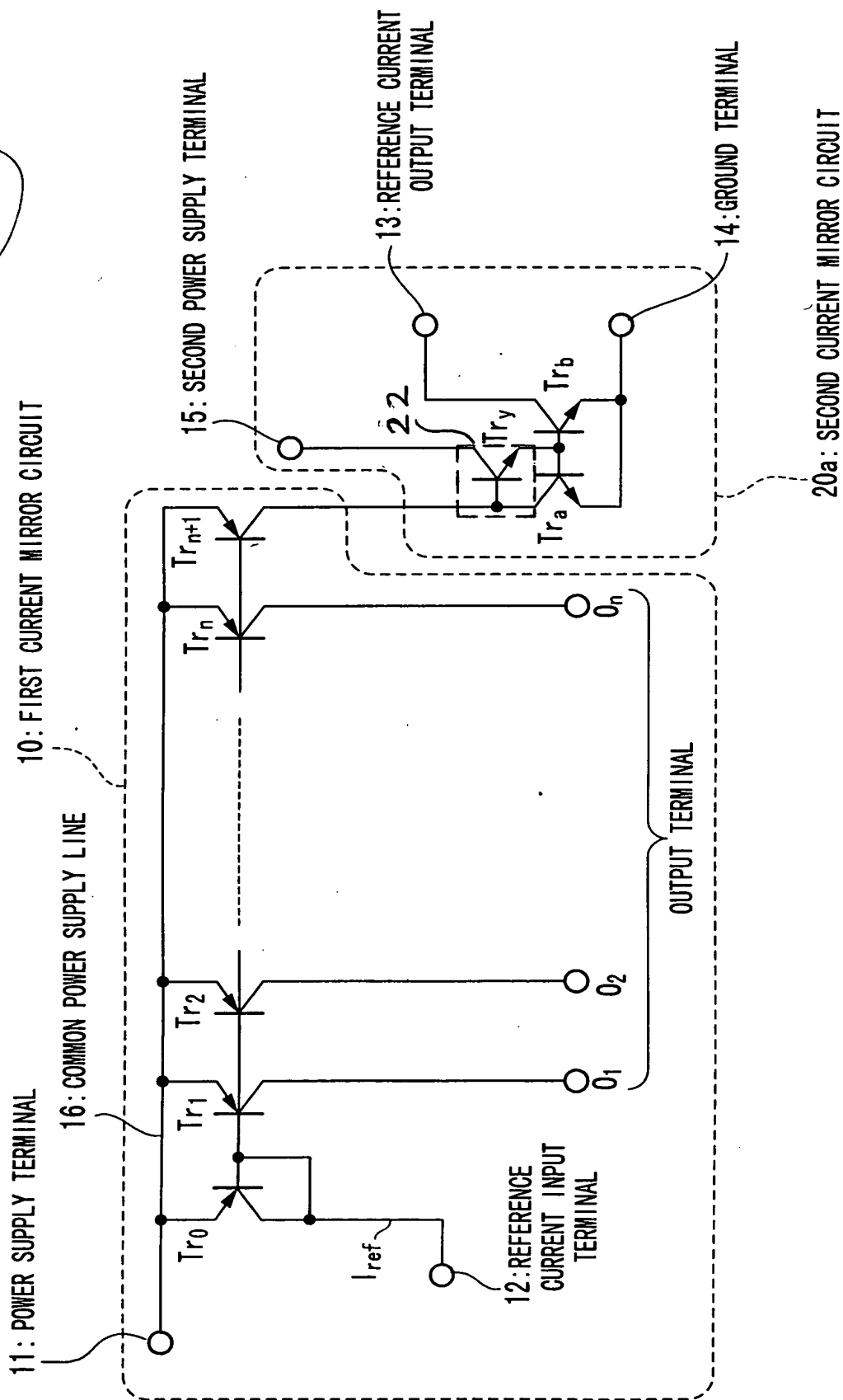
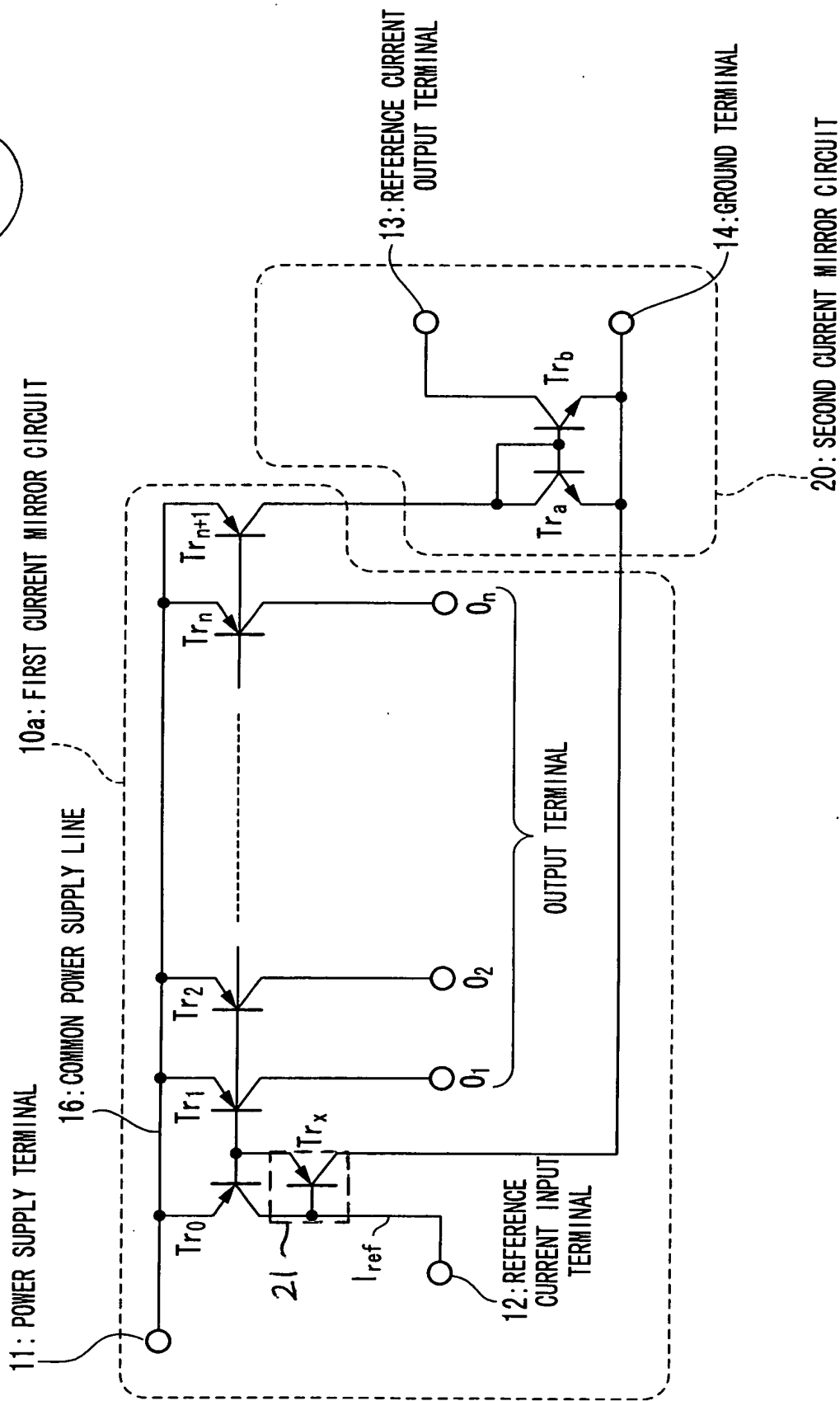


Fig. 8



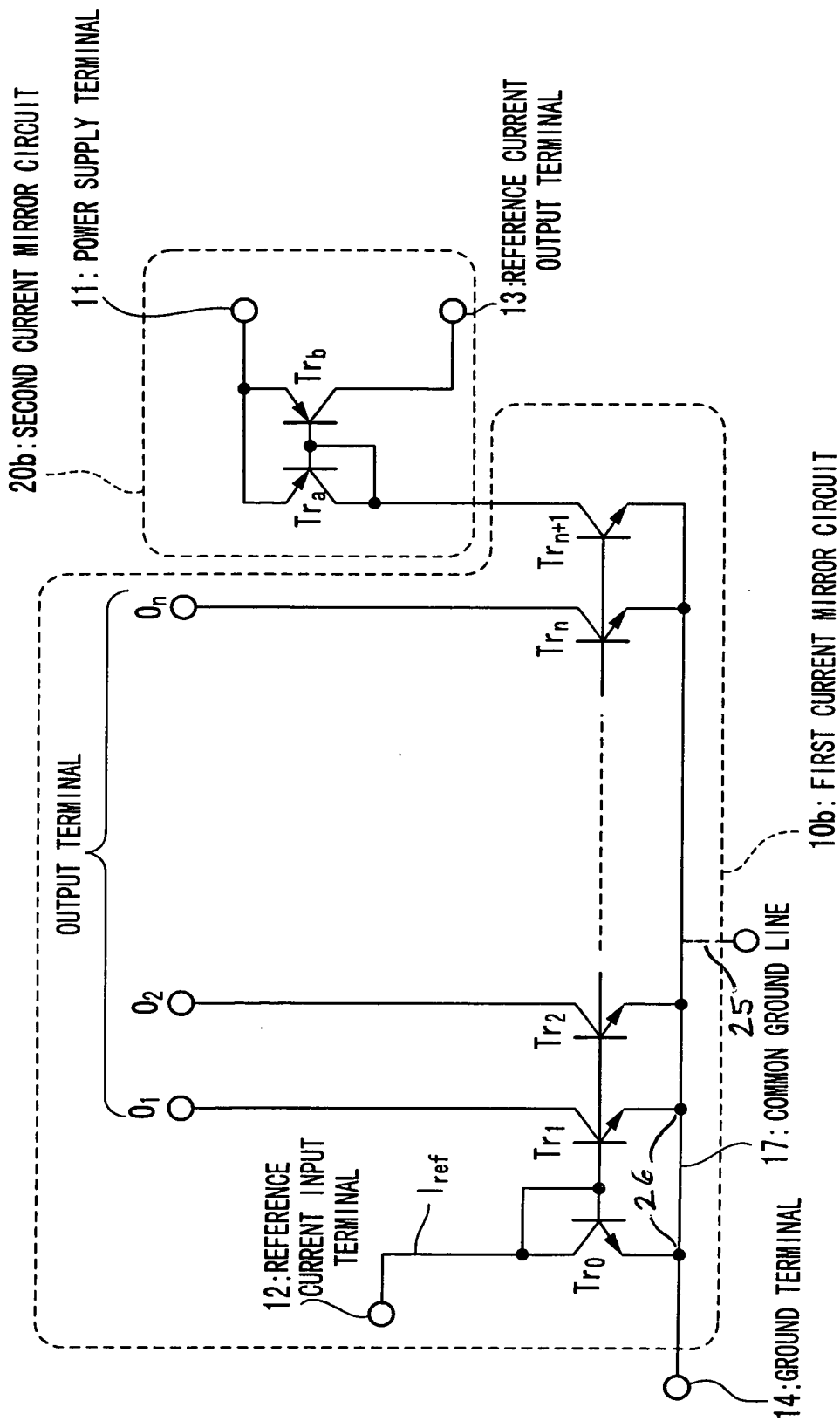
OK
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Fig. 9



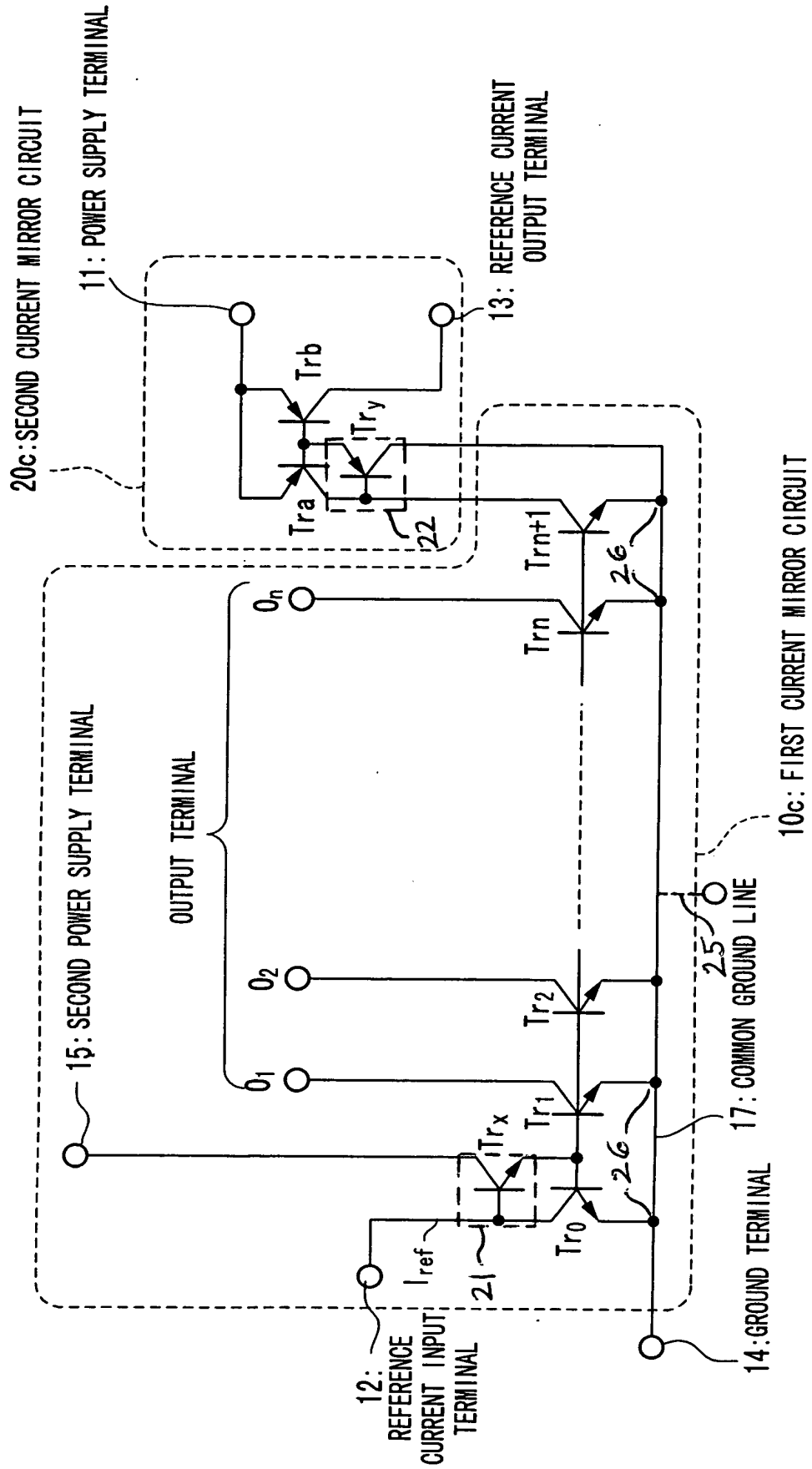
O I P E
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Fig. 10



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Fig. 11



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JAN 07 2003
PATENT TRADEMARK
3/18/03
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Fig. 12

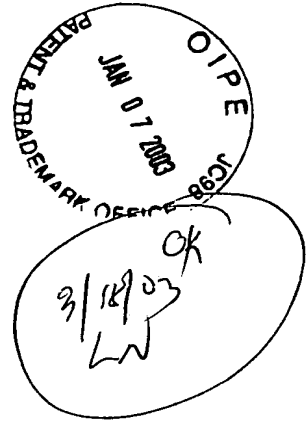
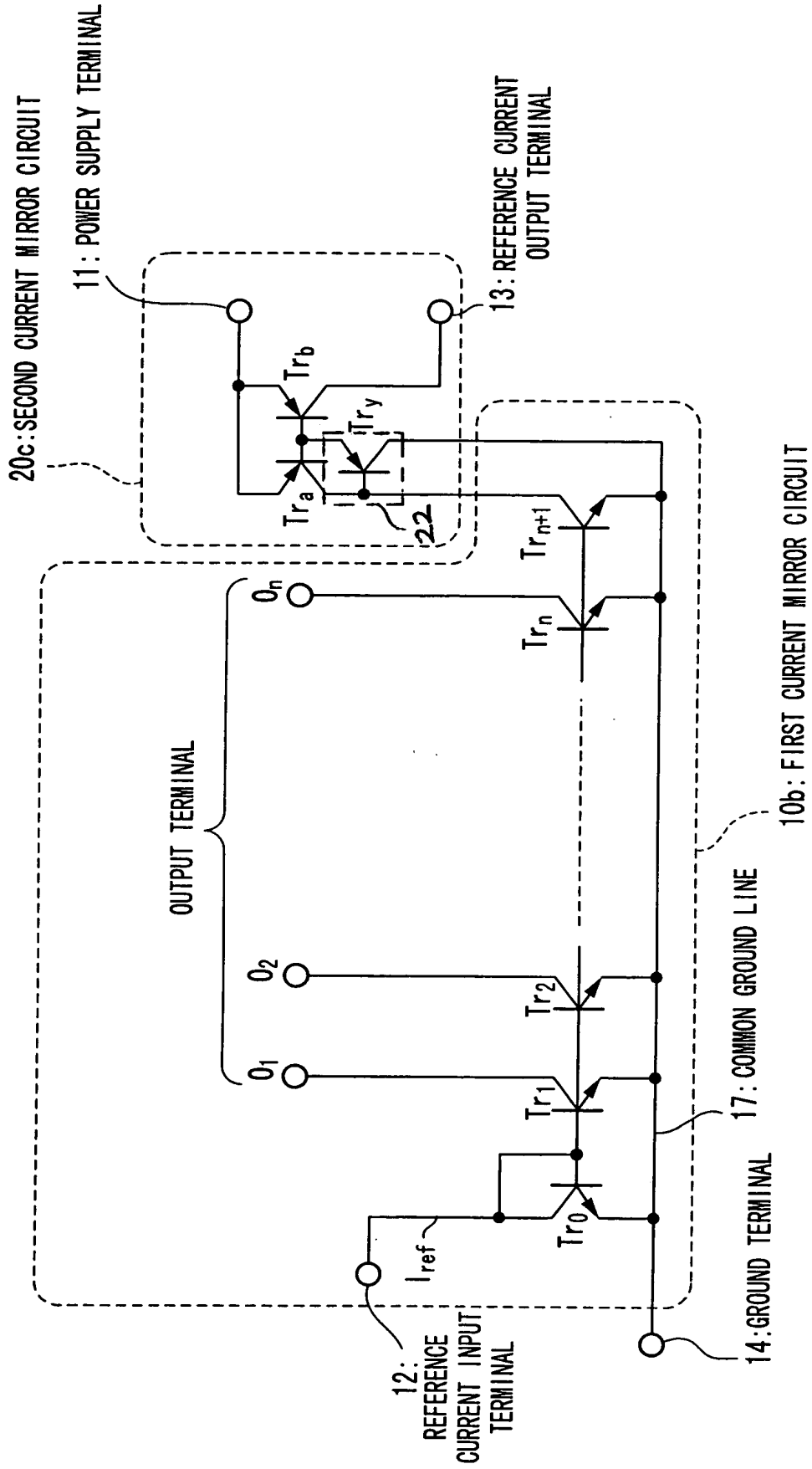


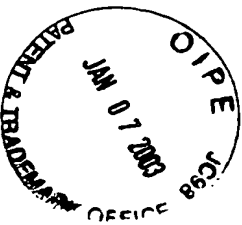
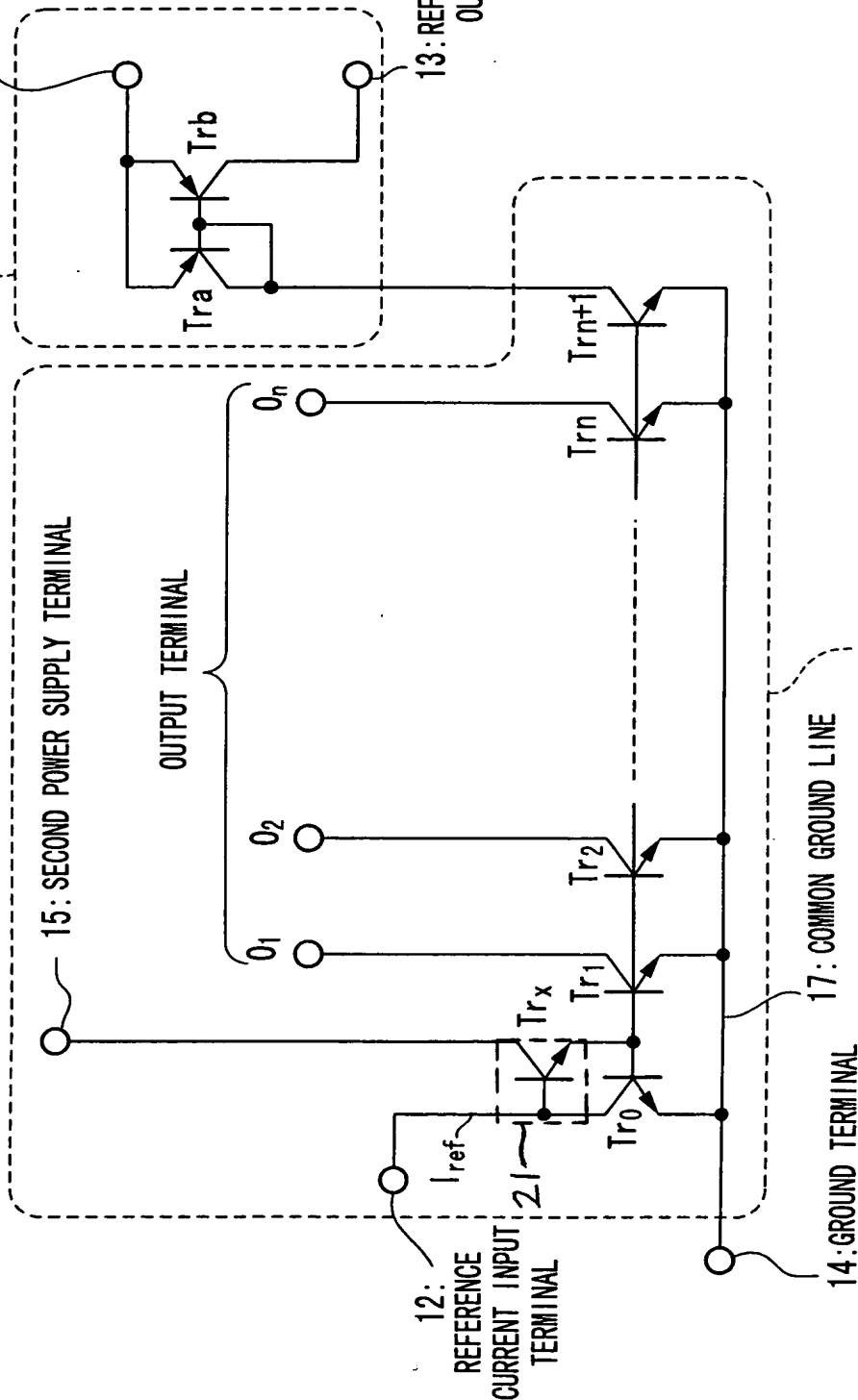
Fig. 13

20b: SECOND CURRENT MIRROR CIRCUIT

11: POWER SUPPLY TERMINAL

13: REFERENCE CURRENT OUTPUT TERMINAL

10c: FIRST CURRENT MIRROR CIRCUIT

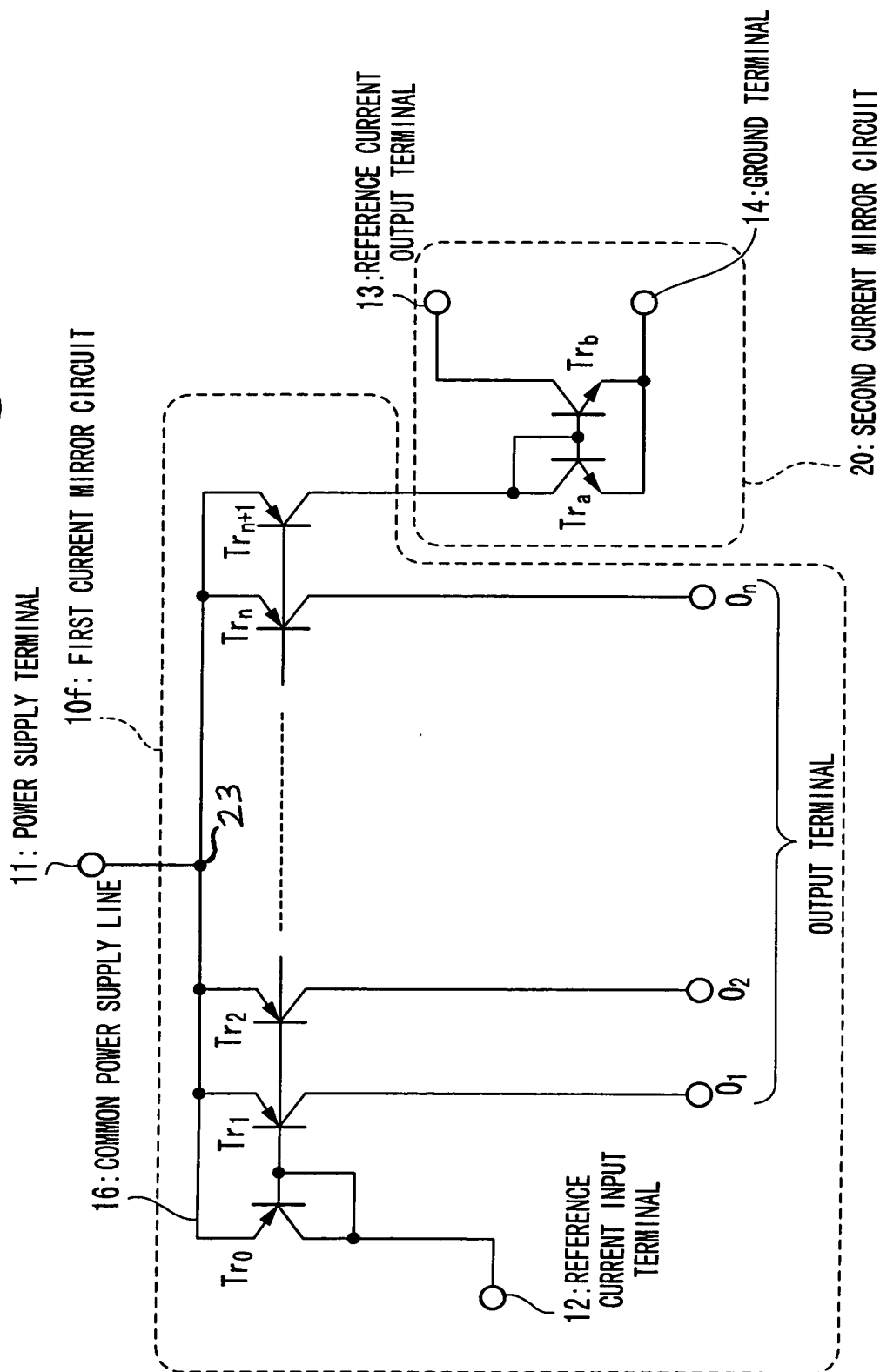


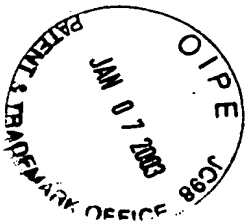
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6h
3/18/03
22

Fig. 16





Oh
3/18/03

Fig. 17

